

MEMORANDUM
September 2, 1980

TO: Steve Fintel Terry Hogan
Zafar Malik Craig Benson

COPY TO: C.B. Wilson Tom Ferrio
Randy Ahlfinger Jim Arnold

FROM: Chuck Branson

SUBJECT: GATE ARRAY DESIGN REVIEW

There will be an electrical design review covering the LONESTAR gate array. This review will be held Thursday September 3, 1981 from 9:00am - 11:00am in Jerry Herzog's conference room. Please review the attached documentation before this time.

Thank You.

Chuck Branson

*9/3/81 = 2.5 MHz ÷ 7. Need some programmability to allow
for changes in processor speed.
• Resolve Rsm timing.*